

Docket No.: MUH-12894

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : MANFRED PRÖLL ET AL.
Filed : CONCURRENTLY HEREWITH
Title : DYNAMIC RAM SEMICONDUCTOR MEMORY AND METHOD FOR
OPERATING THE MEMORY

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

U.S. Patent Application Publication No. 2002/0067653 A1 (Toda), dated June 6, 2002;

U.S. Patent No. 5,798,975 (Büttner et al.), dated August 25, 1998, and corresponding German Published Non-Prosecuted Patent Application, DE 196 32 780 A1 (Büttner et al.), dated February 19, 1998.

As per the Notice in 1273 OG 55 (August 5, 2003) no copies of any above-mentioned U.S. patents and U.S. patent application publications are submitted for any application filed after June 30, 2003.

Respectfully submitted,


For Applicants

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Date: December 1, 2003

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FORM PTO-1449 (SUBSTITUTE) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))				Attorney Docket No.: MUH-12894 Appl. No.: <hr/> Applicant: MANFRED PRÖLL ET AL. <hr/> Filing Date: December 1, 2003 Group Art Unit:			
EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
	A	2002/0067653 A1	6/6/02	Toda			
	B	5,798,975	8/25/98	Büttner et al.			
	C						
	D						
	E						
	F						
	G						
	H						
	I						
FOREIGN PATENT DOCUMENT							
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES NO
	J	196 32 780 A1	2/19/98	Germany			
	K						
	L						
	M						
	N						
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)							
EXAMINER				DATE CONSIDERED			